

The diagram shows a current source 10. It features a differential pair of transistors 35 and 36, a current mirror 37, and a feedback loop 30. The feedback loop consists of a resistor 15, a capacitor 14, and a transconductance stage 13. The output of the feedback loop is connected to the gates of the transistors 35 and 36. The current source 10 is connected to a load 16 and a reference voltage 19.

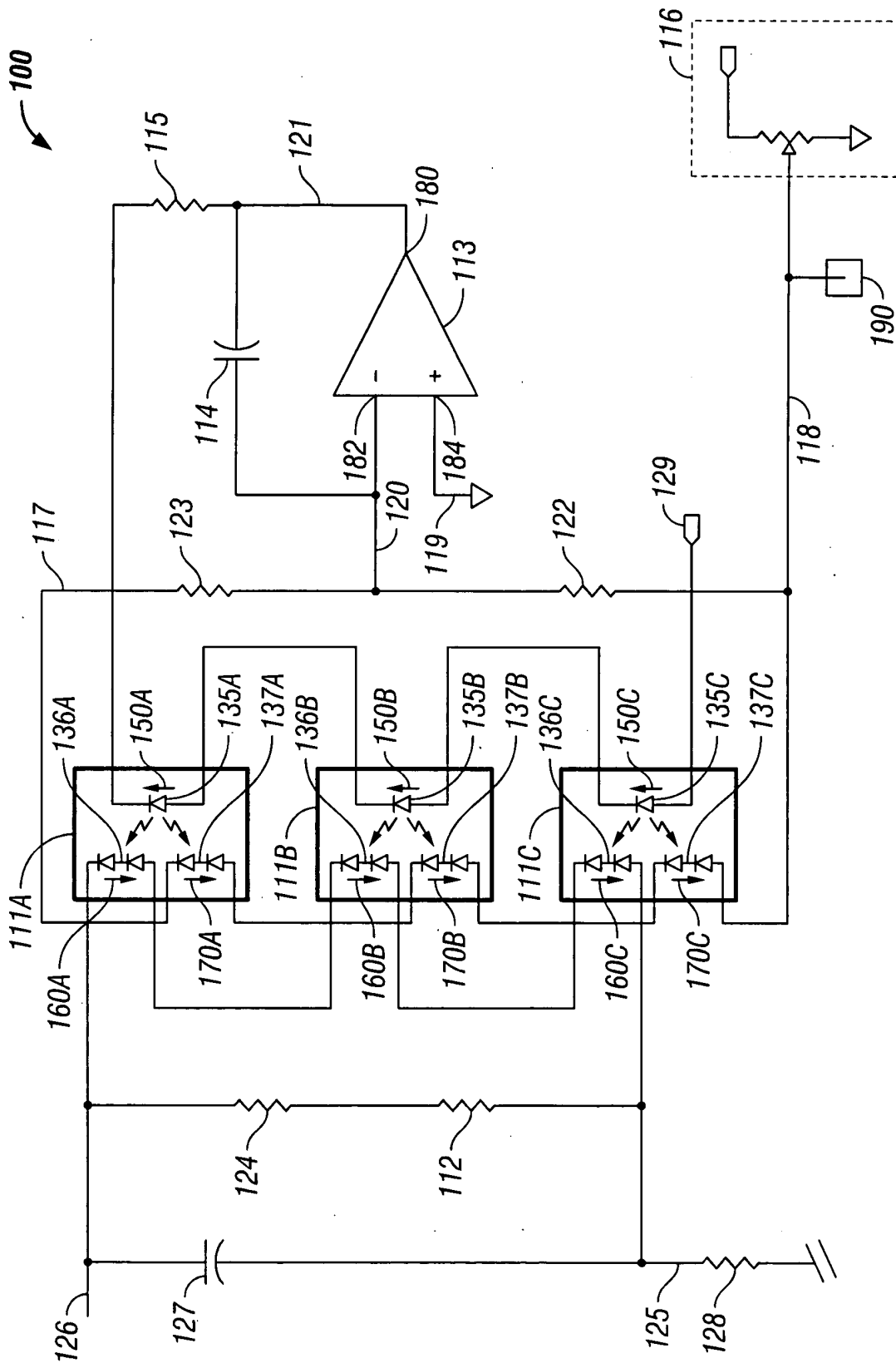


FIG. 2

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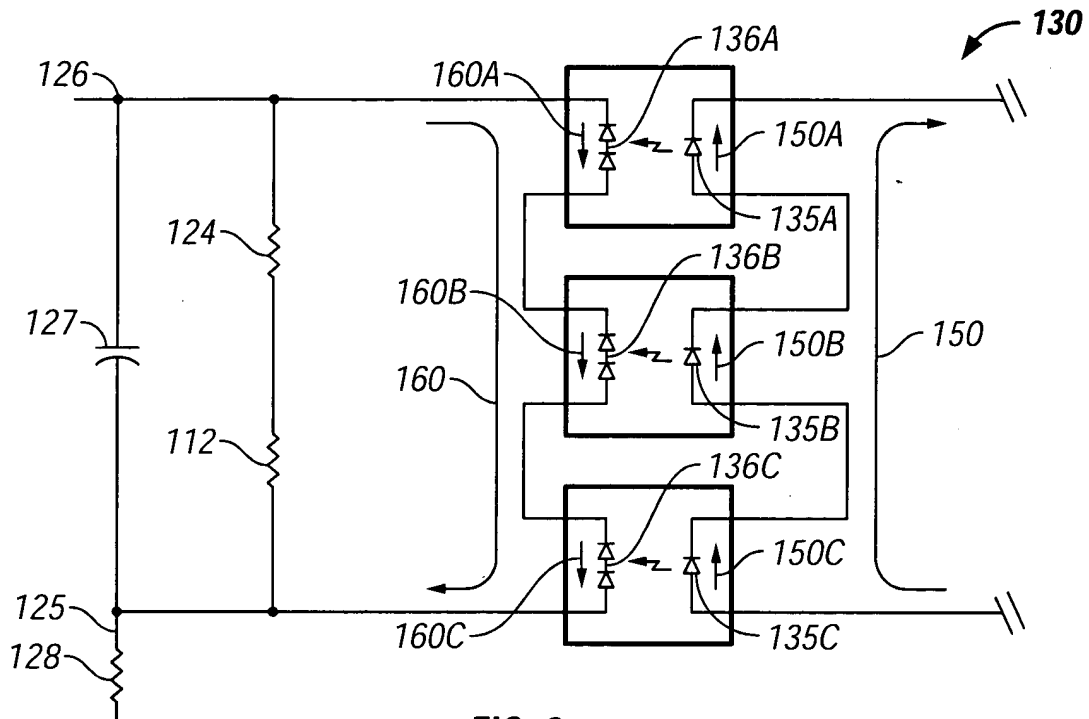


FIG. 3

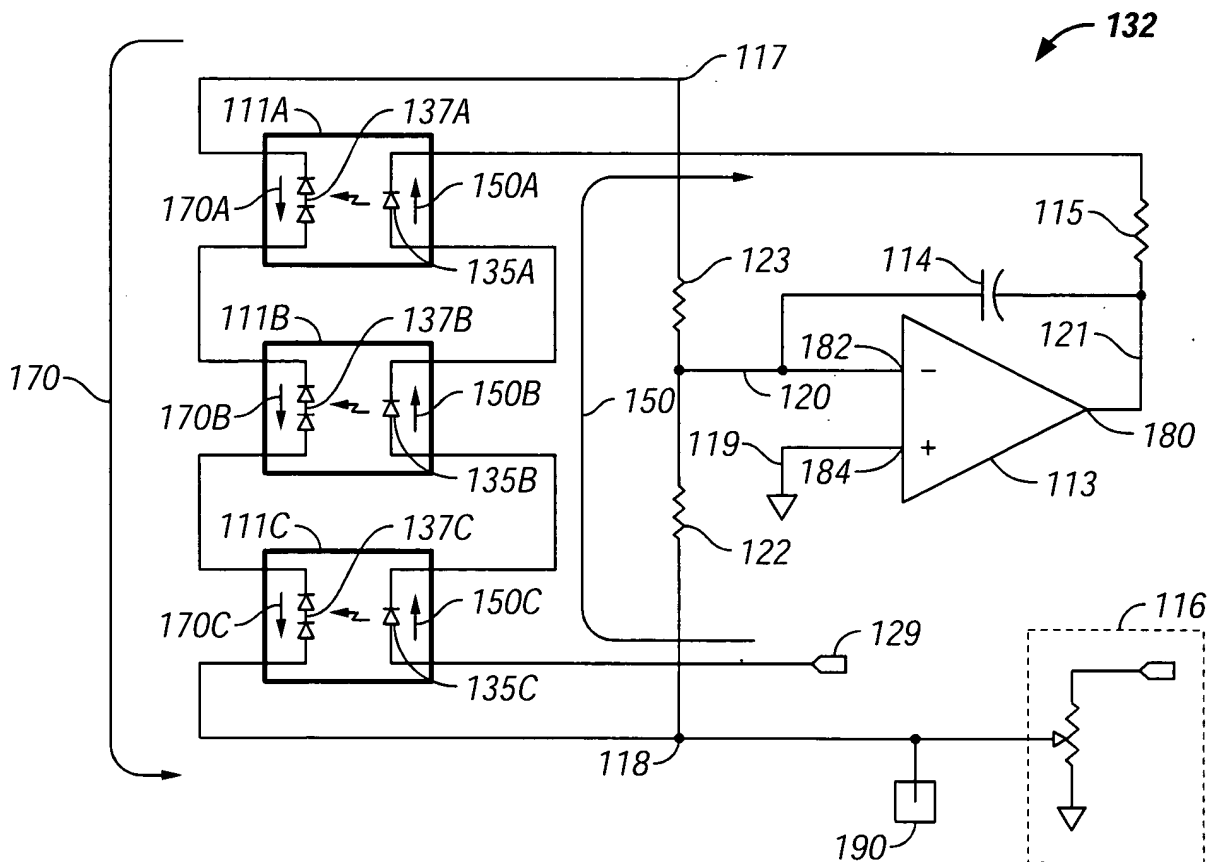


FIG. 4

[illegible]